## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2046	280/735.ccls. and (@ad< "20021219" @rlad< "20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:20
L4	2258	307/10.1.cds. and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:21
L5	1115	340/436.cds. and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:21
L6	1492	340/438.ccls. and (@ad<"20021219" (@rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:21
L7	4	1 and (control\$4 or microcontroller\$4 or CPU\$2 pr processor\$2) and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:22
L8	10	4 and (control\$4 or microcontroller\$4 or CPU\$2 pr processor\$2) and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:22
L9	3	5 and (control\$4 or microcontroller\$4 or CPU\$2 pr processor\$2) and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:22
L10	14	6 and (control\$4 or microcontroller\$4 or CPU\$2 pr processor\$2) and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:22

L11	6	6 and (control\$4 or microcontroller\$4 or CPU\$2 pr processor\$2) and (SPI or (serial with peripheral with interface)) and transceiv\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:23
L12	0	5 and (control\$4 or microcontroller\$4 or CPU\$2 pr processor\$2) and (SPI or (serial with peripheral with interface)) and transceiv\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:23
L13	2	4 and (control\$4 or microcontroller\$4 or CPU\$2 pr processor\$2) and (SPI or (serial with peripheral with interface)) and transceiv\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:23
L14	0	1 and (control\$4 or microcontroller\$4 or CPU\$2 pr processor\$2) and (SPI or (serial with peripheral with interface)) and transceiv\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:23
L15	251	(exchang\$4 or transfer\$4 or interact\$4 or interact\$4 or interexchang \$49 same (control\$4 or microcontroller\$4 or CPU\$2 pr processor\$2 same (SPI or (serial with peripheral with interface)) and transceiv\$4 and (@ad<"20021219" @ilad<"20021219"	US-PCPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:41
L16	988	701/36.cds. and (@ad<"20021219" @rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:41
L17	73	15 and switch\$4 and (deploy \$4 or redundan\$4) and ( impact\$4 or safe\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:42
L18	11	16 and (control\$4 or microcontroller\$4 or CPU\$2 pr processor\$2) and (SPI or (serial with peripheral with interface)) and transceiv\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:42

L19	4	(controlS4 or microcontroller \$4 or CPUS2 pr processor\$2) with (SPI or (serial with peripheral with interface)) with transceiv\$4 with (extchang\$4 or interchang\$4 or transfer\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	MOR	ON	2008/10/17 12:55
L20	43	(control\$4 or microcontroller \$4 or CPU\$2 pr processor\$2) same (\$P1 or (serial with peripheral with interface)) same transceiv\$4 same (exchang\$4 or interchang\$4 or transfer\$4) and (@ad<"20021219" @rlad<"20021219"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 12:58
L21	22	(control\$4 or microcontroller \$4 or CPU\$2 pr processor\$2) same (SPI or (serial with peripheral with interface)) same transceiv\$4 same (exchang\$4 or interchang\$4) and (@ad< "20021219" @rlad<"20021219"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 13:00
83	53	((wireless or inductive) with transmis5) and (controls4 or microcontrols4 or process54 or microprocess54) and ((serial with peripheral with interface) or SPI) and (SOON or (safe54 with (circuit\$2 or conductor\$2 or control\$4))) and (exchang\$4 or transfer \$54) and switch\$4 and (deploy \$4 or redundan\$4) and (@ad<*20021219*) (@inda<*20021219*)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:34
S4	349	((wireless or inductive) with transmi\$5) and (control84 or microcontrol84 or process\$4 or micropcontrol84 or process\$4 or micropcocss\$4) and (sens\$4 with wheel) and (sens\$4 or detect\$4 or trigger\$4) and (exchang\$4 or transfer\$4) and (witch\$4 and (deploy\$4 or redundan\$4) and (impact \$4 or safe\$4) and (@ad<"20021219")  @ad<"20021219"	US-PCPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:35

<b>S</b> 5	6	((wireless or inductive) with transmi\$5) same (control\$4 or process \$4 or microoprocess\$4) same (steer\$4 with wheel) and (seer\$4 with wheel) and (seen\$4 or deted\$4 or trigger \$4) same (exchang\$4 or transfer\$4) same switch\$4 and (deploy\$4 or redundan \$4) and (impact\$4 or safe \$4) and (impact\$4 or safe \$4) and (impact\$4 or safe \$4) and (impact\$4 or \$4] with the process of the proc	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:36
S6	8	S4 and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:40
S7	0	S5 and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:40
S8	53	S3 and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:41
S9	4	S8 and (steer\$4 with wheel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:41
S10	59	kazmierczak-harald.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:45
S11	47	tschentscher-harald.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:46
S12	120	schumacher-hartmut.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:46

S13	251	mattes-bernhard.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:47
S14	1	S10 and ITIC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:47
S15	1	S11 and ITIC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:47
S16	1	S12 and ITIC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:47
S17	1	S13 and ITIC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 12:47
S18	0	S5 and (SPI or (serial with peripheral with interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:04
S19	349	((wireless or inductive) with transmi\$5) and (control\$4 or process\$3 or microportol\$4 or process\$3 or microportol\$4 or process\$3 or detect\$4 with wheel) and (sens\$4 or detect\$4 or trigger\$4) and (exchang\$4 or transfer\$4) and (switch\$4 and (deploy\$4 or redundan\$4) and (impact \$4 or safe\$4) and ([@ad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:04

S20	239	((wireless or inductive) with transmiss) and safe\$3 with (control\$4 or microcontrol\$4 or process\$3 or dister\$4 with wheel) and (sens\$4 or detect\$4 or trigger\$4) and (exchang\$4 or transfer\$4) and switch\$4 and (deploy\$4 or redundan \$4) and (impact\$4 or safe \$4) and (@ad~"20021219" (@rlad~"20021219")	US-PGPUB; USPAT; USCOR; EPO; JPO; DEFWENT; IBM_TDB	OR	ON	2008/10/16 13:12
S21	239	((wireless or inductive) with transmis5) and (sale53 with (control\$4 or microcontrol\$4 or process\$3 or microprocess\$3) and (steer\$4 with wheel) and (sens\$4 or detect \$4 or trigger\$4) and (exchang\$4 or transfe\$4) and (exchang\$4 or transfe\$4) and or redundan\$4) and (impact \$4 or safe\$4) and (@ad<"20021219" (@rida<"20021219" (@rida<"20021219")	US-PGPUB; USPAT; USCOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:12
S22	26	((wireless or inductive) with transmis5) and (safe\$3 with (control\$4 or microcontrol\$4 or process\$3 or microprocess\$3) same (sters\$4 with wheel) and (sens\$4 or detect \$4 or trigger\$4) and (exchang\$4 or detect \$4 or transfer\$4) and switch\$4 and (deploy\$4 or redundan\$4) and impact \$4 or safe\$4) and (@ad<"20021219" (@ida<"20021219" (@rida<"20021219")	US-PGPUB; USPAT; USCOR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16
S23	328	((wireless or inductive) with transmis5) and (safe\$3 with (control\$4 or microcontrol\$4 or process\$3 or microprocess\$3)) same (exchang\$4 or transfer\$4 or swito\$4) and (deploy\$4 or redundan\$4) and (impact\$4 or safe\$4) and (@adx*20021219" @indax*20021219"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OX	2008/10/16 13:20

S24	29	((wireless or inductive) with transmiss) and (safes3 with (control\$4 or microcontrol\$4 or process\$3 or microprocess \$3)) same (exchang\$4 or transfer\$4 or switch\$4) and (\$Pl or (serial with peripheral with interface)) and (deploy\$4 or redundan \$4) and (impact\$4 or safe \$4) and (@adc*20021219*) @rladc*20021219*)	US-PGPUB; USPAT; USCOR; EPO; JPO; DEFIWENT; IBM_TDB	OR	ON	2008/10/16 13:21
S25	207	((Wireless or inductive) with transmi\$5) and (control\$4 or microcontrol\$4 or process\$3 or microprocess\$3) same (exchang\$4 or transfer\$4 or switch\$94) and (SPI or (serial with peripheral with interface)) and (deploy\$4 or redundan\$4) and (impact\$4 or safe\$4) and (impact\$4 or safe\$4 or safe\$4) and (impact\$4 or safe\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:22
S26	55	((Wireless or inductive) with transmi\$5) and (control\$4 or microcontrol\$4 or process\$3 or microprocess\$3) same (exchang\$4 or transfer\$4 or switch\$\$\$4\$) same (\$Pl or (serial with peripheral with interface)) and (deploy\$4 or redundan\$\$4\$) and (impact\$\$4\$ or safe\$\$4\$) and (@ad<"20021219" (@rlad<"20021219")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:23
S27	3	((Wireless or inductive) with transmi\$5) and (control\$4 or microcontrol\$4 or process\$3 or microprocess\$3) same (exchang\$4 or transfer\$4 or switch\$\$\$\$4\$ or same (\$Pl or (serial with peripheral with interface)) same (deploy\$\$\$4\$ or redundan\$\$4\$) and (impact \$4\$ or safe\$\$4\$) and (@ad<"20021219" (@ilad<"20021219")	US-PCPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:23

S28	1	(((wireless or inductive) with transmis5) and (controls4 or process\$3 or microportcess\$3 or microporcess\$3 and (exchang\$4 or transfer\$4 or switch\$4\) and (SPI or (serial with peripheral with interface)) and (deploy\$4 or redundan\$4).cim.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:28
S29	312	(((wireless or inductive) with transmi\$5) and (control\$4 or process\$3 or microprocess\$3) and (exchang\$4 or transfer\$4 or switch\$4) and (deploy\$4 or redundan\$4)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:30
S30	12	S29 and (steer\$4 with wheel)	US-PGPUB; USPAT	OR	ON	2008/10/16 13:30
S31	1	\$10 and redundan\$4 and deploy\$4 and (control\$4 or microprocess\$4 or process \$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 14:13
S32	1	S12 and redundan\$4 and deploy\$4 and (control\$4 or microprocess\$4 or process \$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 14:13
S33	3	\$13 and redundan\$4 and deploy\$4 and (control\$4 or microprocess\$4 or process \$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 14:13
S34	1	S11 and redundan\$4 and deploy\$4 and (control\$4 or microprocess\$4 or process \$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 14:13
<b>S3</b> 5	364	(transceiv\$4 and (control\$4 or microcontrol\$4 or process \$3 or microprocess\$3) and (exchang\$4 or transfer\$4 or switch\$4) and (deploy\$4 or redundan\$4)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 10:34

S36	10	(transceiv\$4 and (control\$4 or microcontrol\$4 or process \$3 or microprocess\$3) and (exchang\$4 or transfer\$4 or switch\$4) and (deploy\$4 and redundan\$4)).clm.	US-PGPUB; USPAT	OR	ON	2008/10/17 10:35
S37	10	S35 and (steer\$4 with wheel)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/17 10:38

<sup>10/17/08 2:04:33</sup> PM

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